

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: )  
Anand Murthy, et al. ) Examiner: Not Yet Assigned  
Application No: Not Yet Assigned ) Art Unit: Not Yet Assigned  
Filed: Herewith )  
For: A SEMICONDUCTOR TRANSISTOR )  
HAVING A STRESSED CHANNEL )  
\_\_\_\_\_ )

Mail Stop Patent Application  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicant hereby requests consideration of this Information Disclosure Statement pursuant to 37 C.F.R. § 197(b). Attached is Form PTO-892 from the parent application filed on November 1, 2001 (Application No. 10/002,465).

EXPRESS MAIL CERTIFICATE OF MAILING

I hereby certify that I am causing this paper or fee to be deposited with the United States Postal Service "Express Mail Post Office to Addressee" service on the date indicated above and that this paper or fee has been addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia, 22313-1450.

July 23, 2003  
Date of Deposit

Linda K. Brost

*Linda K. Brost*  
Signature

Name of Person Mailing Correspondence

*July 23, 2003*  
Date

Pursuant to 37 C.F.R. § 1.98(d), copies of the referenced art are not being provided herewith since they were previously sent to and/or provided by the Patent and Trademark Office. It is respectfully requested that the cited documents be considered in the 1.53(b) continuation application filed herewith and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner and returned to Applicant.

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed as an admission that the information cited in this statement is material to patentability.

Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

  X   37 C.F.R. §1.97(b).

       37 C.F.R. §1.97(c). If so, then enclosed with this Information Disclosure Statement is one of the following:

       A statement pursuant to 37 C.F.R. §1.97(e) or

       A check for \$180.00 for the fee under 37 C.F.R. § 1.17(p).

       37 C.F.R. §1.97(d). If so, then enclosed with this Information Disclosure Statement are the following:

(1) A statement pursuant to 37 C.F.R. §1.97(e); and

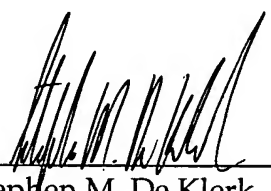
(2) A check for \$       for the fee under 37 C.F.R. §1.17(p) for submission of the Information Disclosure Statement.

If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: July 23, 2003

  
\_\_\_\_\_  
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<b>Notice of References Cited</b>	Application/Control No. 10/002,465	Applicant(s)/Patent Under Reexamination MURTHY ET AL.	
	Examiner Junghwa M. Im	Art Unit 2811	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,698,869	12-1997	Yoshimi et al.	257/192
	B	US-5,763,319	06-1998	Ling et al.	438/514
	C	US-5,990,516	11-1999	Momose et al.	257/327
	D	US-5,841,173	11-1998	Yamashita, Kyoji	257/384
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Kwok K. Ng. "complete guide to semiconductor device", McGraw-Hill, page 614
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.